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D-8000 München 80(DE)(54) **Bonding sheet for electronic component and method of bonding electronic component using the same.**(57) **The bonding sheet (20) of the present invention comprises a substrate having an opening (22), and a low-melting point bonding metal (25) which closes the opening (22) or is arranged on the peripheral portion of the opening, to project in the opening. According to a bonding sheet (20) of the present invention, a low-melting point bonding metal (25) is interposed between a conductor pattern of a substrate (30) and electrode terminals (42) of an electronic component (40), and is bonded by thermocompression at a low temperature without melting the low-melting point bonding metal, so that bonding of a large number of electrode terminals can be completed by a single bonding operation.****EP 0 260 490 A1**

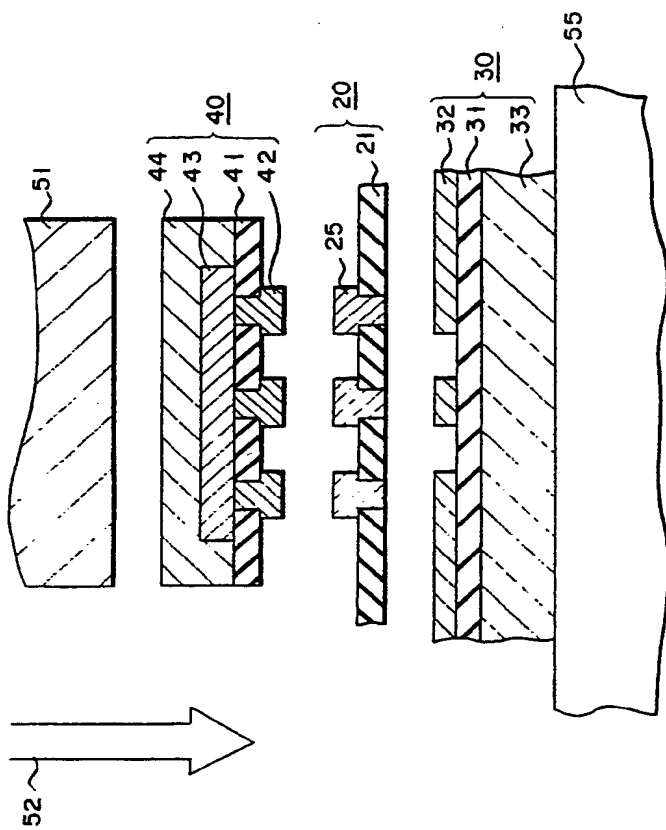


FIG. 3

Bonding sheet for electronic component and method of bonding electronic component using the same

The present invention relates to a bonding sheet for an electronic component, which is used when electrode terminals of the electronic component such as a semiconductor element are electrically connected to a conductive pattern formed on substrate, and a method of bonding an electronic component, using the same.

5 In the course of recent technical developments, semiconductor integrated circuits have been miniaturized at a high density, some integrated circuit elements having 100 terminals or more. Consequently, demand has arisen for the development of techniques for efficiently bonding the high-density integrated circuit element onto a substrate with conductive patterns. Among the techniques developed, a flip-chip method, in particular, which can simultaneously bond a large number of electrode terminals of a semiconductor integrated circuit to conductive patterns of a substrate, has received a great deal of attention.

10 The flip-chip method is a general term of face-down bonding wherein a semiconductor element is bonded to bonding pattern terminals formed on a substrate, while its active surface faces downward. This method can be mainly classified into a method wherein bonding is performed after metal projections (bumps) are formed on electrode terminals on a semiconductor element and a method wherein bonding is performed after bumps are formed on connecting terminals on a substrate with conductive patterns.

15 In order to form bumps, in a known method of the former one, a multilayered structure of Or/Cu/Au or Ti/Ni/Au is formed by deposition, after which a Pb-Sn solder is plated thereover. In a known method of the latter one, a Pb-Sn solder is printed or plated. In either method, formation of bumps requires complicated processes, resulting in a low yield.

20 Using the flip-chip method, electrode terminals of the semiconductor element are bonded to connecting terminals on the substrate with conductive patterns, at a high temperature of 250 to 330°C. However, a solder used for the connecting terminals causes peeling, cracking, and the like, due to temperature distortion (arising from the difference in the thermal expansion coefficient of the semiconductor element and that of the wiring substrate) when the solder is cooled from the high temperature upon melting and bonding to room temperature, resulting in poor reliability. Such a high temperature also degrades a device formed on the substrate. When a liquid crystal display is used for a substrate, an organic color filter is degraded. Since the heat-resistance temperature of a color filter is about 150°C, its characteristics become degraded at the above-mentioned temperature.

Another known method of bonding the electrode terminals of a semiconductor element and connecting 30 the terminals of a substrate or the like is the film carrier method. Using this method, a conductive layer on a resin film is bonded to electrode terminals on a semiconductor element by means of thermocompression bonding. More specifically, a bump formed of, for example, a Ti/Ni/Pd/Au laminated structure is formed on all electrode terminals on a semiconductor element, and Sn is deposited on a Cu conductive layer on a film carrier. Heat at a temperature of 450 to 500°C and a high pressure of 200 to 1000 kg/cm² (20 to 100 g per 35 100 μm \times 100 μm terminal) are applied to both the bump and carrier, so that they are bonded by a gold-tin eutectic. However, as in the case of the flip-chip method, so also in the case of the film carrier method, temperature distortion occurs due to the high temperature. Also, since the above high pressure is applied to the semiconductor element, the element may be damaged.

40 Further, it is difficult to form electrode terminals having a uniform height due to an increase in the number of electrode terminals as described above. Therefore, a high pressure is locally generated between the bump and the connecting terminal (or electrode terminal), and the semiconductor element may be damaged.

It is an object of the present invention to provide a bonding sheet for an electronic component capable of bonding at a low pressure and at a low temperature, and a method of bonding an electronic component 45 using the same.

The bonding sheet of the present invention comprises a substrate having an opening, and a low-melting point bonding metal which closes the opening or is arranged on the peripheral portion of the opening, to project in the opening. The bonding method of the present invention comprises the following steps:

50 a first step of arranging, between electrode terminals of an electronic component and a substrate having a conductor pattern to be electrically connected to the terminals, a bonding sheet for the electronic component, comprising a substrate having an opening, and a low-melting point bonding metal which closes the opening or is arranged on the peripheral portion of the opening to project in the opening and

a second step of bonding the electrode terminals and the conductor pattern through the low-melting point bonding metal.

This invention can be more fully understood from the following detailed description when taken in conjunction with the accompanying drawings, in which:

Figs. 1A and 1B are respectively a plan view and a sectional view of a bonding sheet according to Example 1 of the present invention;

5 Fig. 2 is a view showing the step of forming a bonding metal on a bonding sheet substrate;

Fig. 3 is a view for explaining a bonding method using the bonding sheet;

Figs. 4A and 4B are views for explaining a compression mechanism in the method shown in Fig. 3;

Fig. 5 is a sectional view of a bonding sheet according to Example 2 of the present invention;

10 Figs. 6A and 6B are respectively a plan view and a sectional view of a bonding sheet according to Example 3 of the present invention;

Fig. 7 is a sectional view of a bonding sheet according to Example 4 of the present invention; and

Fig. 8 is a graph showing characteristics of bonding according to the present invention.

According to a bonding sheet of the present invention, a low-melting point bonding metal is interposed between a conductor pattern of a substrate and electrode terminals of an electronic component, and is 15 bonded by thermocompression at a low temperature without melting the low-melting point bonding metal, so that bonding of a large number of electrode terminals can be completed by a single bonding operation. With the bonding method of the present invention, since no bump must be formed on a semiconductor integrated circuit element, defects on the integrated circuit element caused by bump formation cannot occur.

20 The thickness of the sheet used in the present invention preferably falls within the range of 50 to 150 μm , and more preferably, falls within the range of 100 to 130 μm in consideration of a mechanical strength (resistance against torsion, flexure and the like). The shape of the opening can only be suitable for that of an electrode terminal of a semiconductor element, and a circular shape having a diameter of 100 to 200 μm is preferable. A plurality of openings are normally formed at predetermined intervals in correspondenc 25 with a plurality electrode of the semiconductor element.

When a low-melting point bonding metal is formed to close the opening or to project in the opening, the bonding metal is adhered to a substrate with an adhesive, and thereafter, is etched to have a predetermined pattern. A bonding temperature preferably falls within the range of 150 to 155°C if the melting point of the bonding metal is 160°C, and a bonding pressure preferably falls within the range of 50 to 100 kg/cm². Note 30 that the thickness of the bonding metal preferably falls within the rang of 0.2 to 0.5 mm.

The low-melting point bonding metal can be one capable of thermocompression bonding, and in particular, a low-melting point adherent alloy is preferable. Basically, the low-melting point adherent alloy is prepared such that an element for improving a bonding property of an interface is added to a low-melting point solder alloy. The low-melting point solder alloy is prepared such that In, Bi, Cd or the like, in 35 particular, In, for achieving a low melting point is added to a solder of at least two elements selected from the group consisting of Pb, Sn, Zn, Od, and Bi, e.g., Pb-Sn or Sn-Zn.

An example of an element for improving a bonding property at an interface between a low-melting point adherent alloy and an electrode terminal of a semiconductor element and a terminal of a conductive pattern on the substrate is Sb. If Zn, Al, Ti, Si, Cr, Be, or an element such as a rare-earth element having strong 40 affinity with oxygen is further added, a bonding strength can be improved. Unless a thermocompression bonding property as a basic characteristic is greatly changed, the low-melting point adherent alloy may contain another impurity (any element). When an ultrasonic vibration is applied to the low-melting point adherent alloy during a bonding step, the alloy can be strongly bonded to glass. By thermocompression bonding in a semi-molten state, the alloy can be strongly bonded to glass, other oxides, or metals such as 45 Mo, Cr, and Ta which cannot be easily bonded to the Pb-Sn solder. Therefore, the alloy can be easily bonded to metal oxides such as SnO₂, In₂O₃, and ITO (Indium Tin Oxide) known as a transparent electrode material. A bonding mechanism of the adherent alloy can be explained by chemical bond of (Alloy)-(Additive)-O-(Oxide).

Electrode terminals of a semiconductor element are conventionally formed of aluminum. Since the 50 surface of the aluminum electrode terminal is naturally oxidized and an Al₂O₃ film is formed thereon, this mainly causes a degradation in bonding properties. However, since the bonding sheet of the present invention has strong affinity with oxygen, the bonding properties will not be degraded even if the Al₂O₃ film is formed on the electrode terminals. For this reason, the surface of the Al electrode terminal need not be subjected to special surface treatment (deposition of bonding metal, or the like), and the manufacturing 55 processes can be simplified.

Pb, Sn, Zn, Sb and the like constituting an alloy can be appropriately selected, so that an alloy having a softening temperature of 165°C and a melting temperature of about 195°C can be easily obtained. In this invention, bonding by plastic deformation of a low-melting point bonding metal in a semi-molten state is utilized. If this metal is heated to a temperature higher than its melting point, it may be flow d to short-circuit adjacent electrode terminals. Therefore, careful temperature control is required.

According to the present invention, since the bonding metal is in a semi-molten state upon bonding, plastic deformation can be caused at a low pressure. Therefore, semiconductor element will not be damaged. Since the bonding metal is set in the semi-molten state at low temperature, a temperature difference is small if the metal is cooled to a room temperature, and cracking and the like cannot occur. Therefore, the bonding metal of the present invention is suitable for bonding a driving semiconductor integrated circuit element onto a device unsuitable for high-temperature treatment, e.g., a liquid crystal display having an organic color filter. When an electrode substrate of a liquid crystal display is used as a substrate, transparent display electrodes of SnO_2 , In_2O_3 , or ITO serve as connecting terminals.

Even if distances between a large number of electrodes and connecting terminals are not uniform, since the bonding metal is bonded in the semi-molten state, it can be bonded without causing any problem.

Example 1

Fig. 1A is a plan view of bonding sheet 20 according to the present invention, and Fig. 1B is a sectional view taken along a line X - X in Fig. 1A.

A predetermined number of through holes 22 are formed at positions corresponding to electrode terminals of a semiconductor element (the outer shape of the element is indicated by broken line 24) in elongated transparent tape 21 of a polyester resin using sprockets (holes for conveying the tape) 23 as positioning guides. Low-melting point metal 25 is filled in each through hole 22.

Fig. 2 illustrates the manufacture of bonding sheet 20. After sheet-like low-melting point bonding metal 25 is etched by hydrochloric acid-nitric acid mixture to obtain a cleaned surface, it is washed with water and dried (not shown). Then, heat-resistant adhesive 26, which is supplied from container onto a roller 29, is applied to metal 25. Thereafter, tape 21 with through holes 22 and low-melting point bonding metal 25 are adhered to each other through adhesive 26 by rollers 27. In this case, when a pressure is applied to tape 21 and metal 25 by rollers 27 while heating them at a temperature slightly lower than the melting point of metal 25, metal 25 can be filled in through holes 22. Metal 25 on tape 21 is removed by photoetching to leave it around the peripheral portion of each through hole 22 in a square or circular shape (see Fig. 1B). For example, a low-melting point bonding metal containing Pb-Sn-In as a major constituent can be easily etched by hydrochloric acid or the like. Note that since the low-melting point bonding metal has a larger thickness than a normal object to be etched, the metal is not dipped in an etching solution, but the etching solution is sprayed in a direction of thickness of the metal so that undercutting can be prevented. Finally, exposed adhesive 26 and a resist left on metal 25 are removed, thus obtaining a bonding sheet of the present invention.

In Example 1, a low-melting point adherent alloy containing 20% by weight of Pb, 66% by weight of Sn, 10% by weight of In, and 2% by weight of Sb, and 2% by weight of Zn was used. Since this alloy had a softening point of 134°C and a melting point of 160°C, thermo-compression bonding can be performed at a temperature of 150°C.

A bonding method using bonding sheet 20 will now be described. Referring to Fig. 3, in semiconductor integrated circuit element 40, a plurality of Al electrode terminals 42 are formed on oxide film 41 as an insulating layer covering functional circuit portion 43 formed on a portion of the surface of substrate 44. In wiring substrate 80, connecting terminals 32 formed integrally with a conductor pattern on an insulating layer 31 formed on glass substrate 33 are formed in correspondence with electrode terminals 42 of semiconductor element 40.

Upon bonding, substrate 30 is placed on heat block 55, and bonding sheet 20 is arranged between semiconductor integrated circuit element 40 and substrate 30. After electrode terminals 42 of element 40 and metal pieces 25 of sheet 20 are aligned with connecting terminals 32, thermocompression bonding is performed in the direction indicated by arrow 52 by tool 51 incorporating a heater (not shown). In this case, one bonding sheet 20 may be used for each semiconductor element 40. However, when a plurality of semiconductor elements (40) are bonded to substrate 30, one sheet 20 can be used for bonding a plurality of semiconductor elements (40) if connecting terminal patterns on substrate 30 for bonding with a plurality of semiconductor elements are matched with through holes 22 of bonding sheet 20.

The thermocompression bonding is performed as shown in Fig. 4A. Electrode terminal 42 of element 40 is pressed by tool 51 against connecting terminal 32 through low-melting point bonding metal piece 25 in the through hole at a pressure of 1 to 10 g per electrode terminal which is $100\text{ }\mu\text{m} \times 100\text{ }\mu\text{m}$.

Substrate 30 on which connecting terminals 32 are formed is fixed to heat block 55, and a temperature bias of 100 to 120°C is applied in advance to the substrate. When the heater incorporated in tool 51 is driven, metal piece 25 can be heated to 145 to 150°C, i.e., above a softening point and below a melting point, within several seconds (using a pulse heat method for ease of operation). After the predetermined temperature is reached, heating by the heater is stopped, and the structure is cooled below a softening point temperature of 134°C while being compressed. Finally, tool 51 is removed.

Fig. 4B shows a state wherein low-melting point bonding metal piece 25 is plastically deformed and extends via through hole 22 in bonding sheet 20, and connecting terminal 32 of substrate 80 and electrode terminal 42 on semiconductor element 40 are electrically bonded to each other through metal piece 25.

Thereafter, an unnecessary portion of the bonding sheet can be removed, and thus, the bonding operation can be completed. The bonded portion can be protected by a potting agent, thus further improving reliability.

Example 2

Referring to Fig. 5, low-melting point bonding metal pieces 125 close corresponding through holes 122 and are formed on polyester resin tape 121 to cover through hole 122 without being filled therein. In a bonding method using bonding sheet 120, each low-melting point bonding metal piece 125 is filled in corresponding through hole 122 upon heating and compression of the bonding operation, and the electrode terminals of a semiconductor integrated circuit element and connecting terminals of a substrate are electrically connected to each other through corresponding metal pieces 125.

Example 3

Referring to Figs. 6A and 6B (sectional view taken along a line Y - Y in Fig. 6A), through hole 222 of bonding sheet 220 is not closed by a low-melting point bonding metal, but low-melting point bonding metal pieces 225 extending in through hole 222 are arranged around each through hole 222.

In polyester resin tape 221, each through hole 222 is formed into a square shape using sprockets 223 as positioning guide in correspondence with a size of a semiconductor element (the outer shape of the element is indicated by broken line 224). The size of through hole 222 is preferably slightly smaller than the outer shape of the semiconductor element since short-circuiting of the surrounding portion can be advantageously prevented although the thickness of tape 221 slightly disturbs bonding. A low-melting point bonding metal foil (Pb-Sn-Zn alloy foils containing Sb are adhered) on tape 221 is etched by photoetching to leave portions corresponding to the electrode terminals of a semiconductor element around through hole 222, thus forming metal pieces 225.

Bonding sheet 220 is arranged between a substrate and a semiconductor integrated circuit element in the same manner as described above, and after the electrode terminals on the semiconductor element, the low-melting point bonding metal foil pieces of the bonding sheet, and the connecting terminals of the substrate are aligned, they are thermocompression-bonded by the tool.

Example 4

Referring to Fig. 7, in substrate 330, conductive pattern 332 is formed on a flexible substrate 331. A plurality of Al electrode terminals 342 are formed on oxide film 341 as an insulating layer covering functional circuit portion 343 formed on a portion of the surface of substrate 344. Conductive pattern 332, electrode terminals 342, and low-melting point bonding metal pieces 325 projecting from polyester resin tape 321 of bonding sheet 320 are aligned while being observed on the side of glass plate 350 from the direction indicated by arrow 352, and are compressed by tool 372 in the direction indicated by arrow 307. In example 4, no heat block is necessary upon bonding, and semiconductor element 340 is heated by tool 355 supporting element 340.

In the above examples, the cases have been exemplified when bonding is performed in air. However, bonding may be performed in an inert gas atmosphere. This method is effective for preventing oxidation of a micropattern.

Indicated below in Table are contact resistance values between the bonding metal described in Example 1 and various typical materials.

Table

Material	Resistance ($\Omega \cdot \text{cm}^2$)
ITO	$10^{-3} - 10^{-4}$
Al	$10^{-5} - 10^{-6}$
Au	$- 10^{-6}$
Sn	$10^{-6} - 10^{-7}$

As indicated in Table above, a practical bonding can also be attained in terms of electrical resistance by using the bonding sheet of the present invention.

In particular, when the bonding metal used in the invention is bonded with Au or Sn, an intermetallic bonding can be obtained, resulting in an ideal electrical connection. Thus, when Sn or Au is formed on Al electrode terminals on a semiconductor element, a lower resistance and higher reliability can be obtained. In this case, Au or Sn may be formed to a thickness of only about 500 to 5000 Å, and its formation is easier than bump formation.

Incidentally, three samples were prepared by bonding the bonding metal of Example 1 having a thickness of 0.2 mm to a Sn substrate. The bonded area in each sample was 1 mm². The samples were tested for their lifetime on reliability under accelerated conditions at temperature of 80°C and a humidity of 95%. The results are shown in Fig. 8.

Claims

1. A bonding sheet (20, 220) for an electronic component, comprising a substrate (21, 221) having an opening (22, 222), and a low-melting point bonding metal (25, 225) which closes said opening (22) or projects from a peripheral portion of said opening (222) of said substrate (221), toward an inner portion of said opening (222).
2. A sheet according to claim 1, characterized in that said low-melting point bonding metal (25, 225) comprises a low-melting point adherent alloy.
3. A sheet according to claim 2, characterized in that said low-melting point adherent alloy contains, as major constituents, a low-melting point solder alloy, and an element for improving bonding strength at an interface between said alloy and an electronic component to be bonded thereto.
4. A sheet according to claim 3, characterized in that said low-melting point solder alloy contains, as major constituents, in and two elements selected from the group consisting of Pb, Sn, Zn, Cd, and Bi.
5. A sheet according to claim 3, characterized in that said element for improving the bonding strength at the interface is Sb.
6. A sheet according to claim 2, characterized in that said low-melting point adherent alloy contains, as major components, a low-melting point solder alloy, an element for improving bonding strength at an interface between said alloy and an electronic component to be bonded thereto, and an element having a strong affinity with oxygen.
7. A sheet according to claim 6, characterized in that said low-melting point solder alloy contains, as major components, in and two elements selected from the group consisting of Pb, Sn, Zn, Cd, and Bi.
8. A sheet according to claim 6, characterized in that said element for improving the bonding strength at the interface is Sb.
9. A sheet according to claim 6, characterized in that said element having the strong affinity with oxygen is an element selected from the group consisting of Zn, Al, Ti, Si, Cr, Be, and rare-earth elements.

10. A method of bonding an electronic component comprising:

a first step of arranging, between electrode terminals (42) of the electronic component (40) and a conductor pattern (32) to be electrically connected to said terminals (42), a bonding sheet (20) for an electronic component, comprising a substrate (21) having an opening, and a low-melting point bonding metal (25) closing said opening or projecting from a peripheral portion of said opening of said substrate toward an inner portion of said opening; and

a second step of bonding said electrode terminals (42) and said conductor pattern (32) through said low-melting point bonding metal (25).

11. A method according to claim 10, characterized in that in the bonding performed in the second step, said low-melting point bonding metal is heated to a temperature below the melting point thereof, and said electrode terminals and said conductor pattern are thermocompression-bonded to each other.

12. A method according to claim 10, characterized in that said conductor pattern (32) comprises a transparent conductive film.

13. A method according to claim 12, characterized in that said transparent conductive film is of a material selected from the group consisting of SnO_2 , In_2O_3 , and ITO.

14. A method according to claim 10, characterized in that said electronic component (40) is a semiconductor integrated circuit element.

15. A method according to claim 14, characterized in that said electrode terminals of said electronic component are formed of Al.

16. A method according to claim 14, characterized in that said electrode terminals of said electronic component comprise a multilayered structure of Al and Sn.

17. A method according to claim 14, characterized in that said electrode terminals of said electronic component comprise a multilayered structure of Al and Au.

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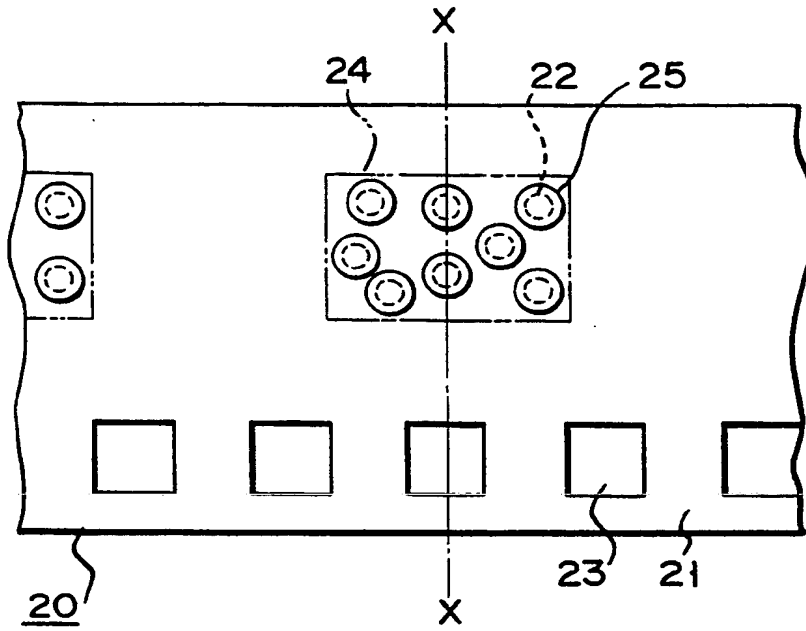


FIG. 1A

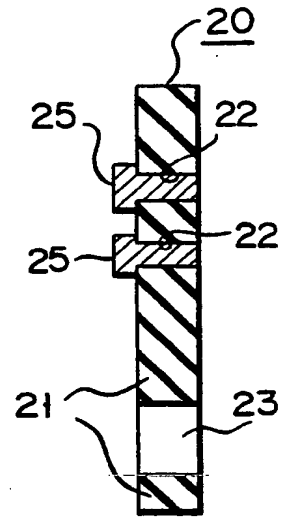


FIG. 1B

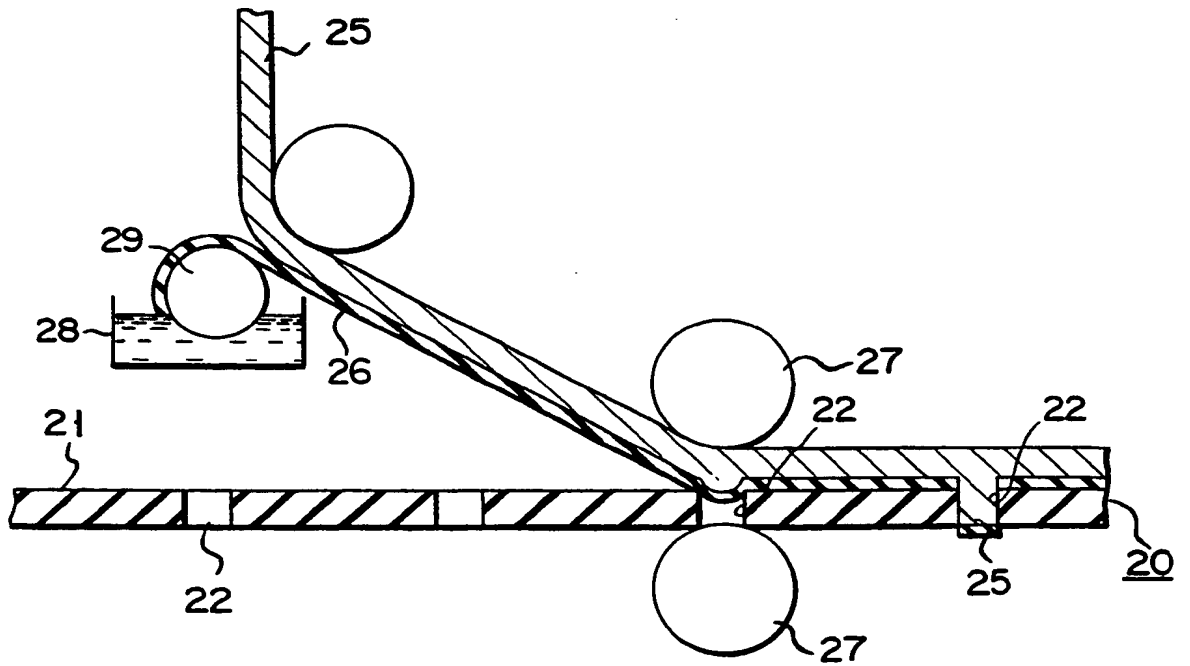


FIG. 2

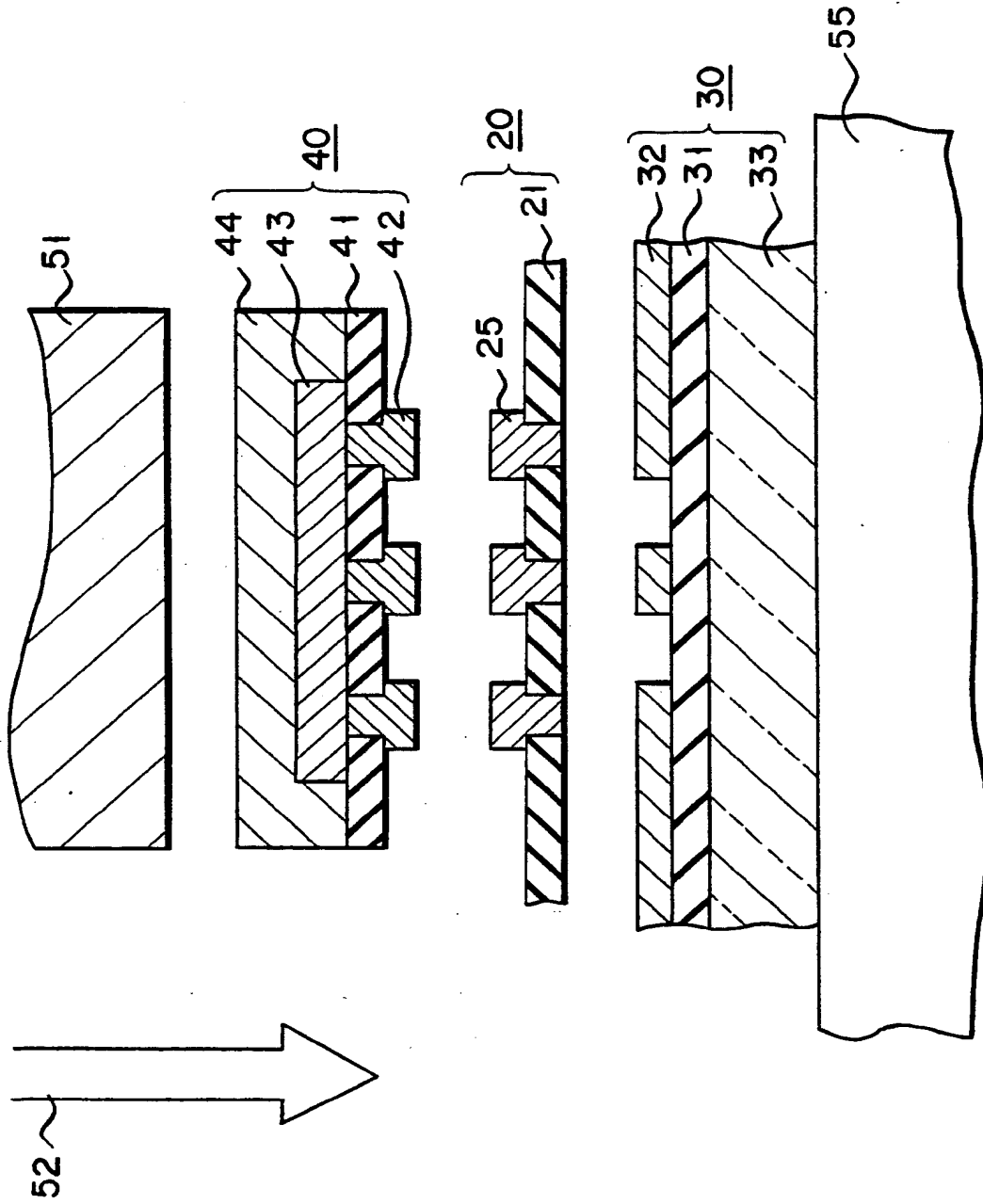


FIG. 3

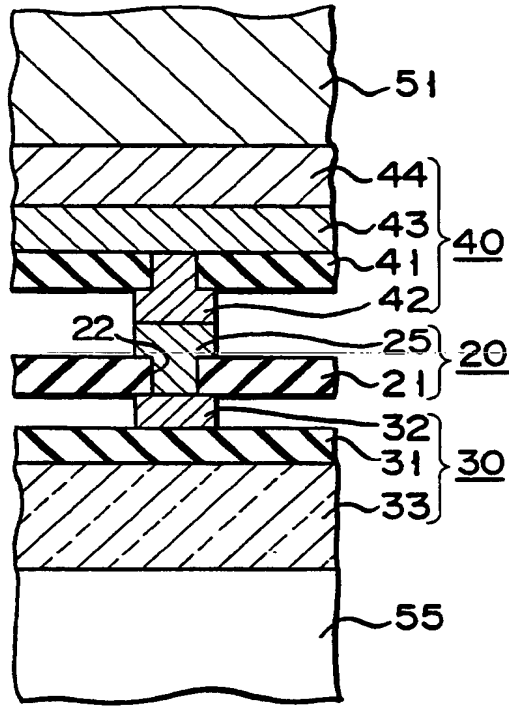


FIG. 4A

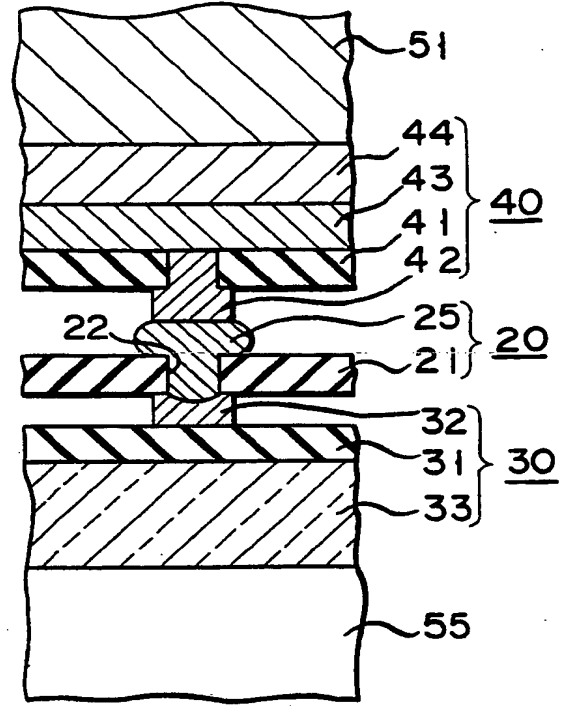


FIG. 4B

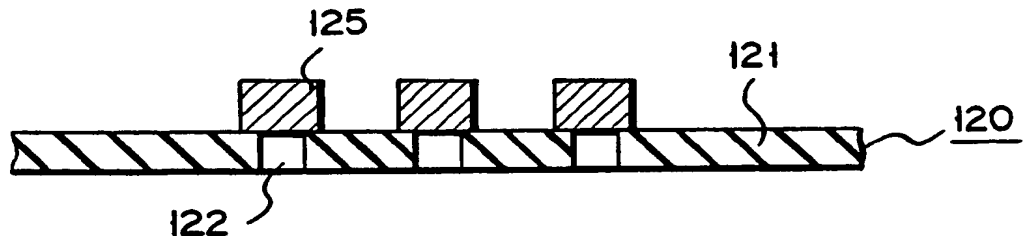


FIG. 5

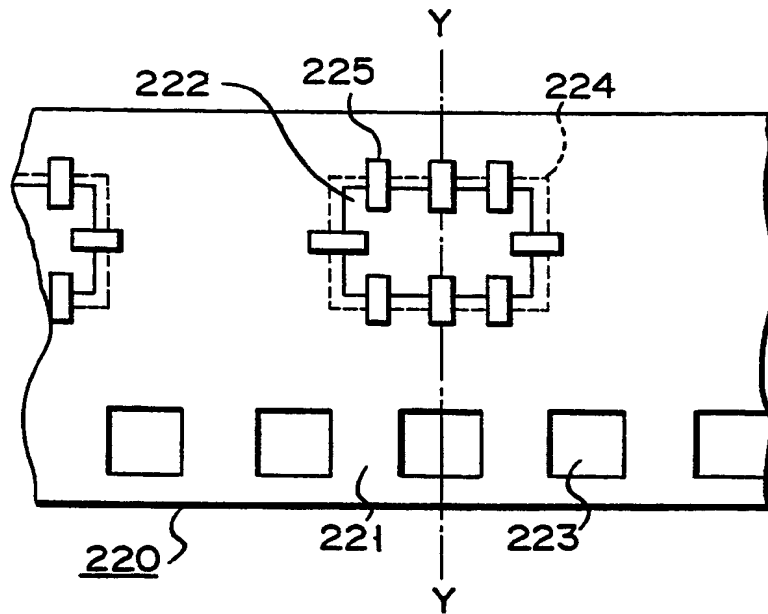


FIG. 6A

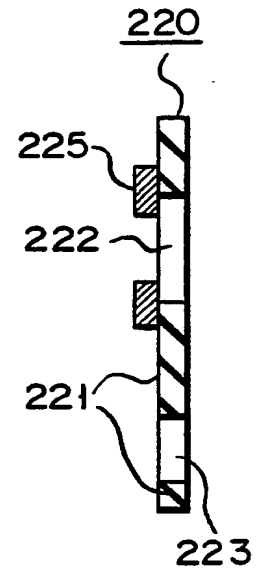


FIG. 6B

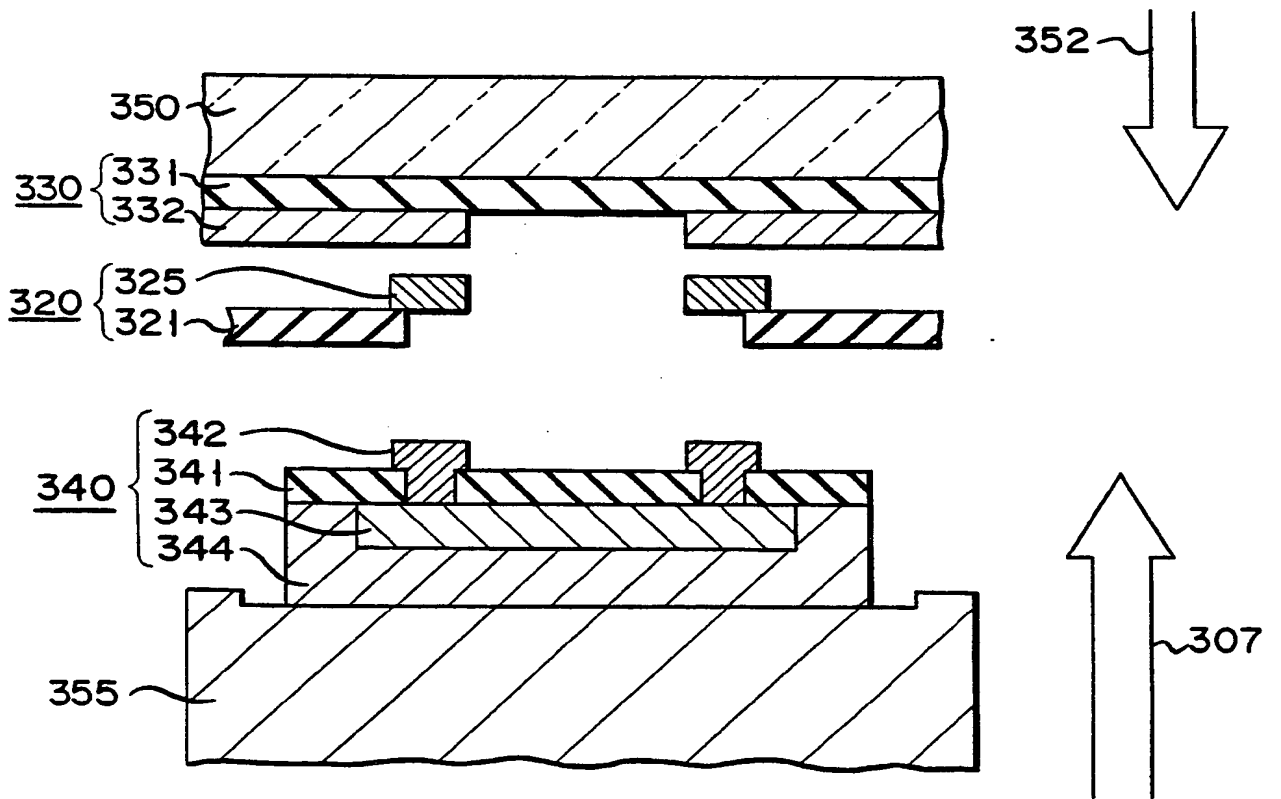


FIG. 7

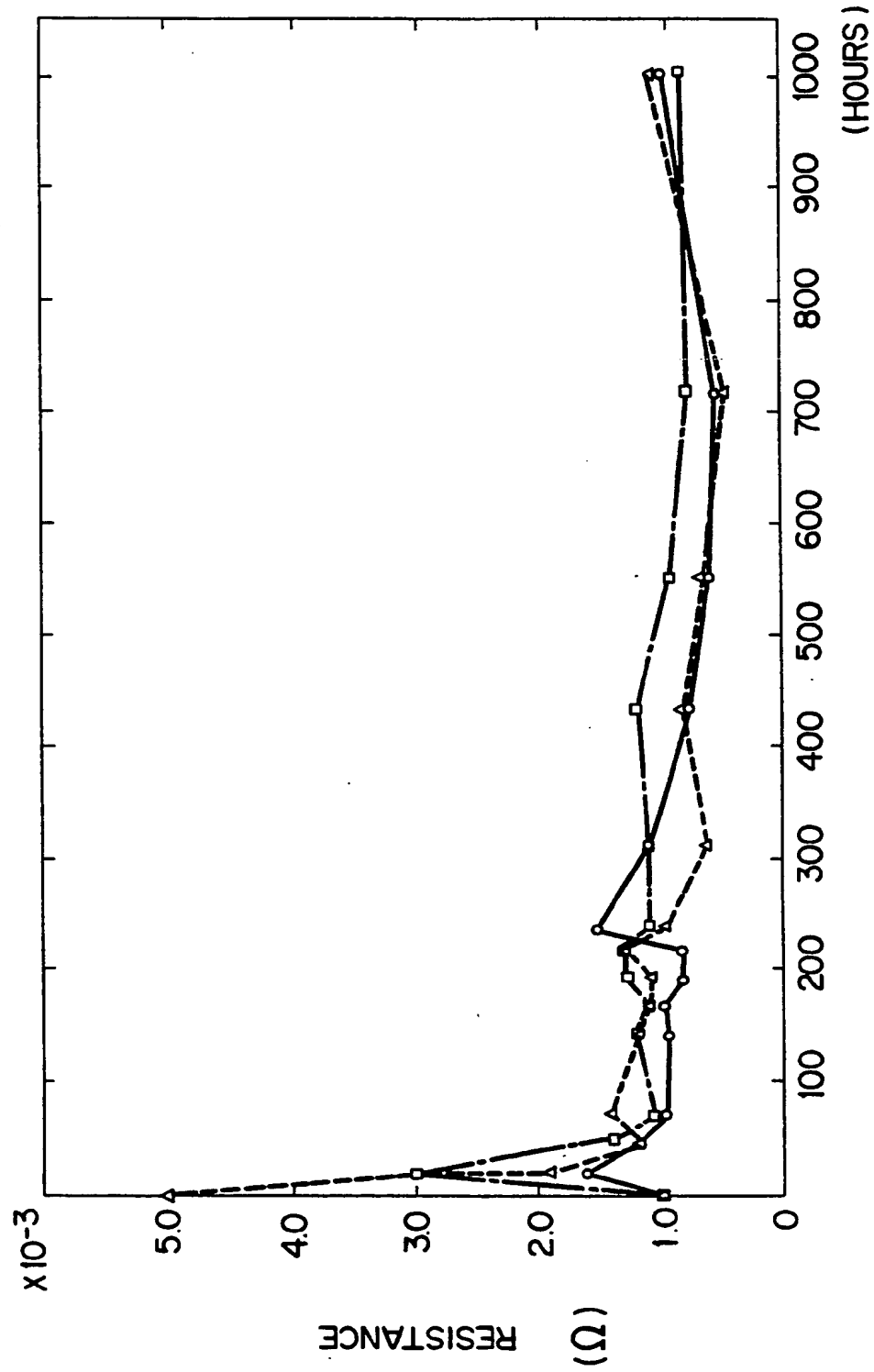


FIG. 8



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number

EP 87 11 2338

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
A	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 28, no. 3, August 1985, pages 1184,1185, New York, US; "Chip carrier of prefabricated glass layers provided with inner vias and conductors" * Page 1184 * ----		H 01 L 23/48 H 01 L 21/60
A	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 27, no. 8, January 1985, page 4855, Armonk, New York, US; K. HINRICHSMAYER et al.: "Solder-filled elastomeric spacer" * Page 4855 * ----		
A	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 17, no. 2, July 1974, page 627, Armonk, New York, US; J.R. GETTEN et al.: "Interface array connector system" * Page 627 * ----		
A	EP-A-0 171 662 (IBM) ----		TECHNICAL FIELDS SEARCHED (Int. Cl.4)
A	DE-A-1 919 567 (SIEMENS) ----		H 01 L
A	FR-A-2 112 466 (HONEYWELL INFORMATION SYSTEMS ITALIA S.p.A.) -----		
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 03-12-1987	Examiner CAMPLING N.C.G.
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